

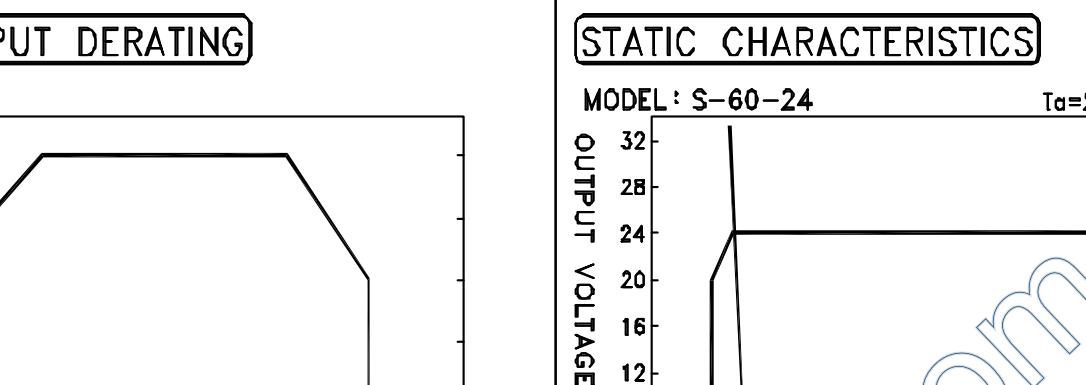
.105C OUTPUT CAPACITOR	.100% FULL LOAD BURNTEST
.INTERNATIONAL AC INPUT RANGE	.BUILT IN EMI FILTER, LOW RIPPLE NOISE
.HIGH EFFICIENCY, LOW WORKING TEMPERATURE	
.SOFTSTART CIRCUIT, LIMITING AC SURGE CURRENT	9001 CERTIFIED MA
.SHORT CIRCUIT, OVERLOAD, OVER VOLTAGE PROTECTED	



DC OUTPUT VOLTAGE	5V	12V	15V	24V
OUTPUT V. TOLERANCE	±2%	±1%	±1%	±1%
OUTPUT RATED CURRENT	12A	5A	4A	2.5A
OUTPUT CURRENT RANGE	0-12A	0-5A	0-4A	0-2.5A
RIPPLE & NOISE	120mV <sub>pp</sub>	120mV <sub>pp</sub>	150mV <sub>pp</sub>	150mV <sub>pp</sub>
LINE REGULATION	±0.5%	±0.5%	±0.5%	±0.5%
LOAD REGULATION	±1%	±0.5%	±0.5%	±0.5%
DC OUTPUT POWER	60W	60W	60W	60W
EFFICIENCY	73%	76%	77%	79%
DC VOLTAGE ADJ.	+10.5%	±10%	±10%	±10%
INPUT VOLTAGE RANGE	85-264VAC 47-63Hz; 120-370VDC			
AC CURRENT	2A/115V1A/230V			
INRUSH CURRENT	COLD STAR 30A/115V 60A/230V			
LEAKAGE CURRENT	<3.5mA/240VAC			
OVERLOAD PROTECTION	105%~150% TYPE: PULSING HICCUP SHUTDOWN: 100%~150% AUTO RECOVERY			
OVER VOLTAGE PROTECTION	115%~135%			
OVER TEMP. PROTECTION	100°C~150°C			
TEMP. COEFFICIENT	±0.03% /°C (0~50°C)			
SETUP, RISE, HOLD UP TIME	800ms, 50ms, 10ms / 115V 300ms, 50ms, 80ms / 230VAC			
VIBRATION	10~500Hz, 2G 60min./1cycle, PERIOD FOR 60min. EACH AXES			
WITHSTAND VOLTAGE	I/P/O/P: 3kVAC/PFG: 1.5kVAC/PFG: 0.5kVAC			
ISOLATION RESISTANCE	I/P/O/P, I/PFG, O/PFG: 500VDC / 100M Ohms			
WORKING TEMP., HUMIDITY	-10°C~+60°C (REFER TO OUTPUT DERATING CURVE) 0~20%RH			
STORAGE TEMP., HUMIDITY	-20°C~+85°C, 10%~95% RH			
DIMENSION	159*97*38mm CASE: 901			
WEIGHT	0.55Kgs			
SAFETY STANDARDS	UL1012, TUV EN60950, IEC950, UL1950 APPROVED			
EMC STANDARDS	CISPR22 (EN55022), IEC280, IEC555 VERIFICATION			
NOTE	1. ALL PARAMETERS ARE SPECIFIED AT 230VAC INPUT, RATED LOAD, 25°C AMBIENT. 2. TOLERANCES INCLUDE SET UP TOLERANCE, LINE REGULATION, LOAD REGULATION. 3. RIPPLE & NOISE ARE MEASURED AT 20MHz BY USING A 12" TWISTED PAIR TERMINATED WITH A 0.1uF & 47uF CAPACITOR. 4. LINE REGULATION IS MEASURED FROM LOW LINE TO HIGH LINE AT RATED LOAD. 5. LOAD REGULATION IS MEASURED FROM 0% TO 100% RATED LOAD. 6. C2,3,6 MUST BE REMOVED.			
				20001003

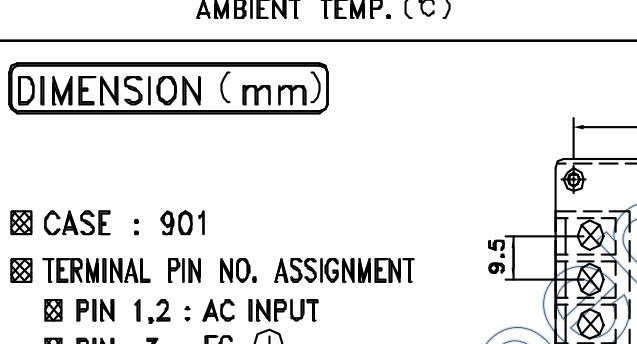
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graph TD
    SW[SWITCH-ING] --> R1[R & FILTER]
    SW --> R2[R & FILTER]
    R1 --> D1[DETECTOR]
    R2 --> D2[DETECTOR]
    D1 --> LOG[LOGIC]
    D2 --> LOG
  
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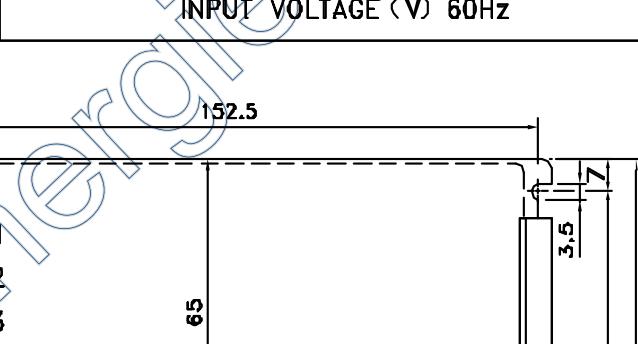


0 240 260

-10 0 10 20 30  
-10 0



60 80 100 120 140 160 180 200 220



■ PIN 3 : FG  
■ PIN 4 : DC OUTPUT  
■ PIN 5 : D.C. BATTERY

