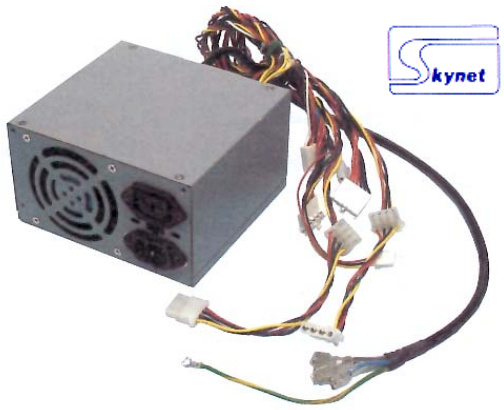
# Industrial PC

**250W ~ 400W**
**AC to DC Series**



## Description :

**This series is designed for industrial PC. It is PS2 size mounting.**

**SNP-AT30, SNP-AX40 are with soft switching topology, high efficiency, universal input and active PFC.**

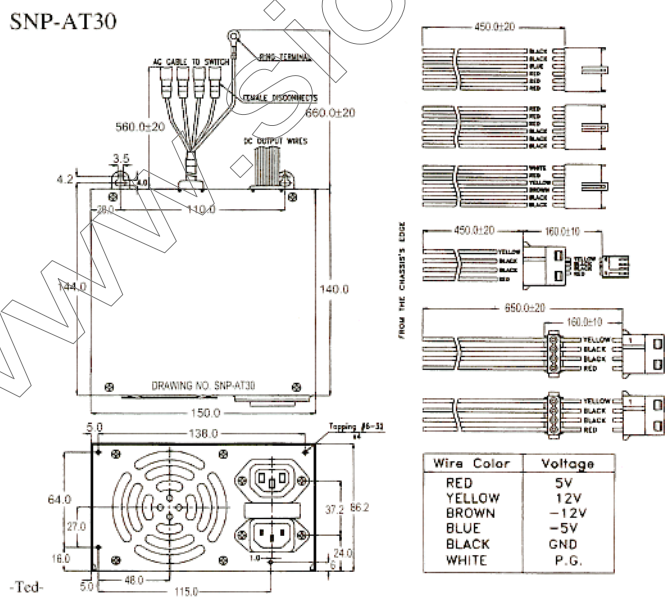
## General specifications :

Input voltage …115V/230V auto-switching circuit for SNP-825A
115/230V by switch for SNP-930A
universal for SNP-AT30, -AX40
Input frequency……..…….47 Hz to 63 Hz
Outputs……..……..see output table
Efficiency……..>65% at rated load for SNP-825A,-930A
>75% at rated load for SNP-AT30, -AX40
Short circuit protection……..…..auto-recovery
Over load protection.....auto-recovery
Over voltage protection.....latch off

Operating temperature……0 to 50°C
Cooling…… Forced air convection
Inrush current…..less than 30A at 115VAC
less than 60A 230VAC
Hold up time…longer than 16ms at rated load and 115VAC
Storage temperature…….-40°C to +85°C
Humidity……up to 95% non condensing
EMI radiation……..EN55022 "B", FCC "B"
EMS.....EN61000-4-2, -3,-4,-5,-6,-8,-11
Safety.....meet UL 60950
CSA 22.2 N°.234
EN 60950

## Mechanical specifications :

SNP-AT30




**Notes:**

1. Dimensions shown in mm (inch) as above.
   Tolerance specified is ± 0.8mm.
2. Size: 150 x 140 x 86.2 (mm)
3. DC Input : using terminal blocks
4. DC Output :
   ATX : Molex 39-01-2200 or equivalent
   AT : Burndy GTC6P-1 or equivalent
   Disk driver : AMP 1-486424-0 or equivalent
   3 1/2 floppy driver : AMP 171822-4 or equivalent



**SICO Energie** – *Conversion d'énergie & composants bobinés*
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## Output Specifications:

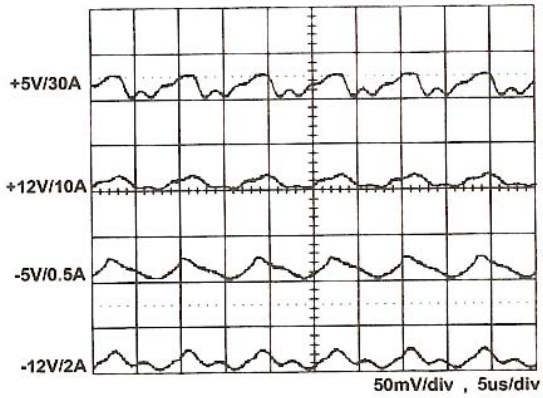
| MODEL NO. | OUTPUT RAIL | LOAD MIN. | LOAD RATED | LOAD MAX. | VOLTAGE ACCURACY | RIPPLE NOISE | LINE REG. | LOAD REG. |
|---|---|---|---|---|---|---|---|---|
| SNP-825A | +5V | 2A | 25A | 30A | +4.80V~+5.20V | 50mV | ±1% | ±3% |
| | +12V | 0.1A | 10A | 15A | +11.40V~+12.60V | 100mV | ±1% | ±5% |
| | -12V | 0A | 0.5A | 1A | -11.40V~-12.60V | 100mV | ±1% | ±2% |
| | -5V | 0A | 0.5A | 1A | -4.75V~-5.25V | 100mV | ±1% | ±2% |
| | +3.3V | 1A | 8A | 15A | +3.13V~+3.47V | 50mV | ±1% | ±2% |
| | +5Vsb | 0A | 0.72A | 1.2A | +4.75V~+5.25V | 100mV | ±1% | ±3% |
| SNP-930A | +5V | 1A | 33A | 40A | +4.90V~+5.10V | 50mV | ±1% | ±3% |
| | +12V | 0.1A | 10A | | +11.28V~+12.72V | 140mV | ±2% | ±5% |
| | -12V | 0A | 1A | | -10.80V~-13.20V | 120mV | ±1% | ±5% |
| | -5V | 0A | 0.5A | | -4.75V~-5.25V | 50mV | ±1% | ±1% |
| SNP-AT30 | +5V | 3A | 30A | 40A | +4.90V~+5.30V | 50mV | ±1% | ±3% |
| | +12V | 0.1A | 10A | 12A | +11.28V~+12.72V | 100mV | ±1% | ±5% |
| | -12V | 0A | 2A | 3A | -11.00V~-13.00V | 100mV | ±1% | ±5% |
| | -5V | 0A | 0.5A | | -4.75V~-5.25V | 50mV | ±1% | ±3% |
| SNP-AX40 | +5V | 2A | 30A | 40A | +4.80V~+5.20V | 50mV | ±1% | ±5% |
| | +12V | 0.2A | 12A | 15A | +11.40V~+12.60V | 100mV | ±1% | ±5% |
| | -12V | 0A | 1A | | -11.40V~-12.60V | 100mV | ±1% | ±2% |
| | -5V | 0A | 0.5A | | -4.75V~-5.25V | 100mV | ±1% | ±3% |
| | +3.3V | 0.5A | 25A | 30A | +3.13V~+3.47V | 50mV | ±1% | ±3% |
| | +5Vsb | 0A | 2A | | +4.75V~+5.25V | 50mV | ±1% | ±1% |

**Notes:**

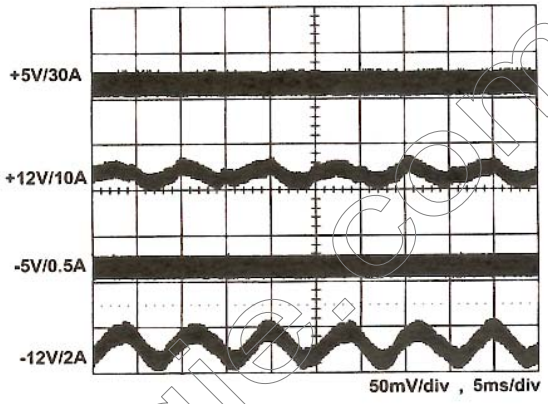
1. Each output can provide up to max load separately when the power supply starts up. To exceed the max. output power continuously is not allowed.
2. At factory, all outputs in 60% rated load condition, each output is checked to be within the accuracy range while the main output is setting to within the specified accuracy range at rated load.
3. Line regulation is defined by changing ±10% of input voltage from nominal line at rated load.
4. Load regulation is defined by changing ±40% of measured output load from 60% rated load at another output set to 60% rated load.
5. Ripple & noise is measured by using 15MHz bandwidth limited oscilloscope and terminated each output with a 0.47μF capacitor at rated load and nominal line.
6. Hold up time is measured from the end of the last charging pulse to the time which the main output drops down to low limit of main output at rated load and nominal line.
7. Efficiency is measured at rated load and nominal line.

## Performance for SNP-AT30:

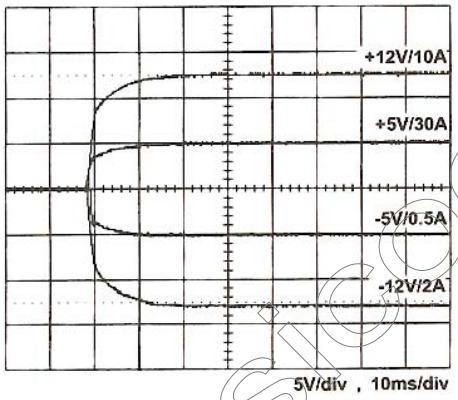
1. Switching frequency ripple



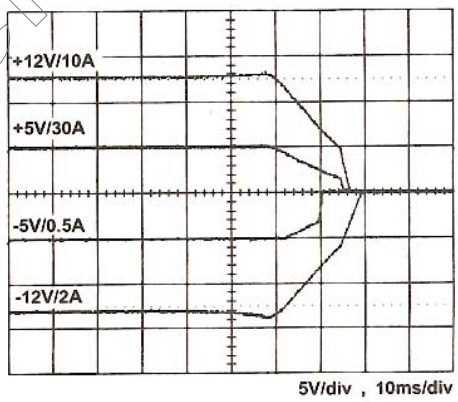

2. Line frequency ripple



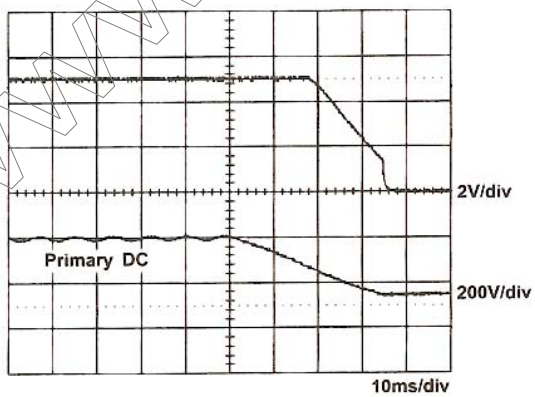

3. Output turn on wave form



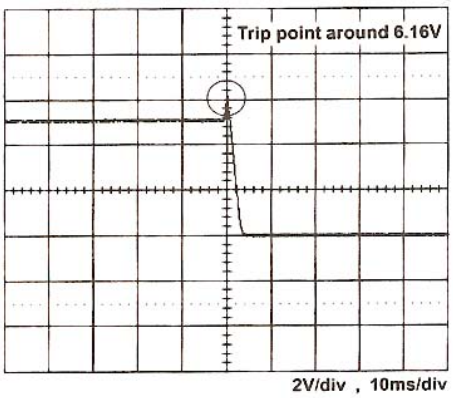

4. Output turn off wave form



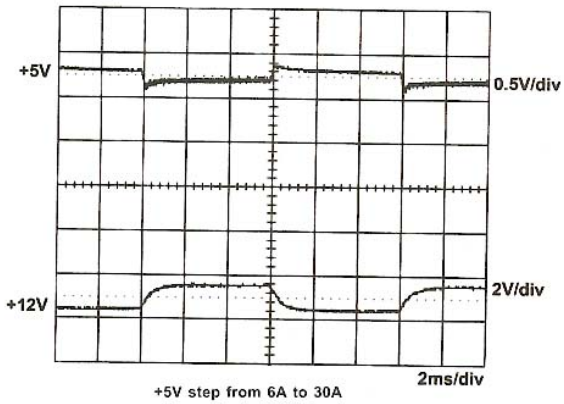

5. Hold-up time




6. Over voltage protection

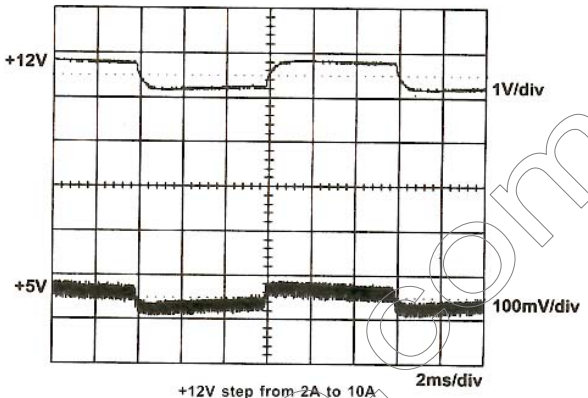


7. +5V step response



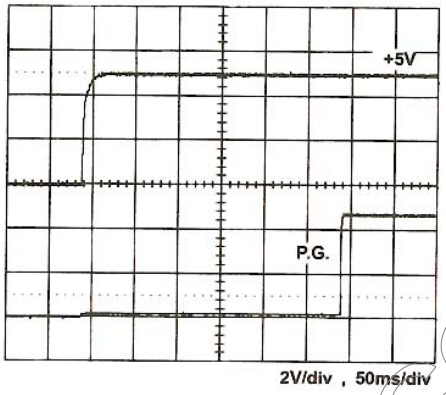

+5V step from 6A to 30A
other output at 60% load

8. +12V step response

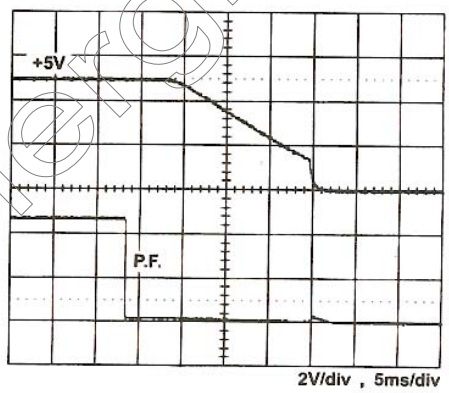



+12V step from 2A to 10A
other output at 60% load

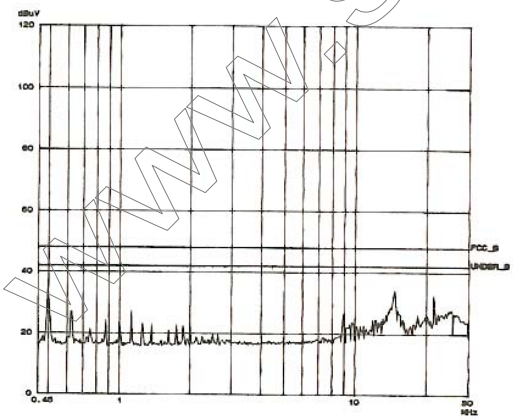
9. Power good signal




10. Power fail signal




11. FCC B



12. EN 55022 B